

Figure 1

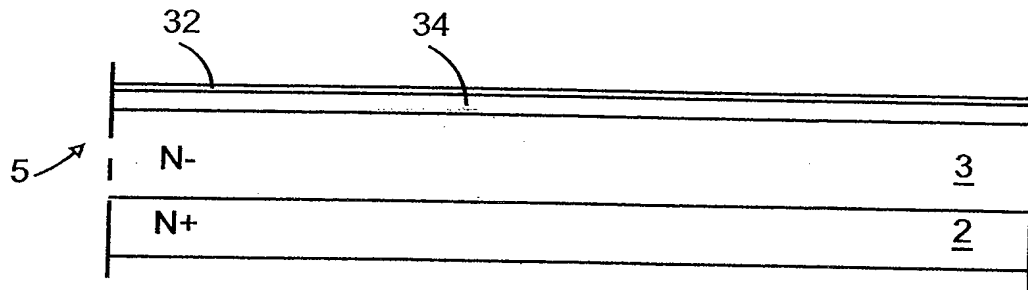


Figure 2a

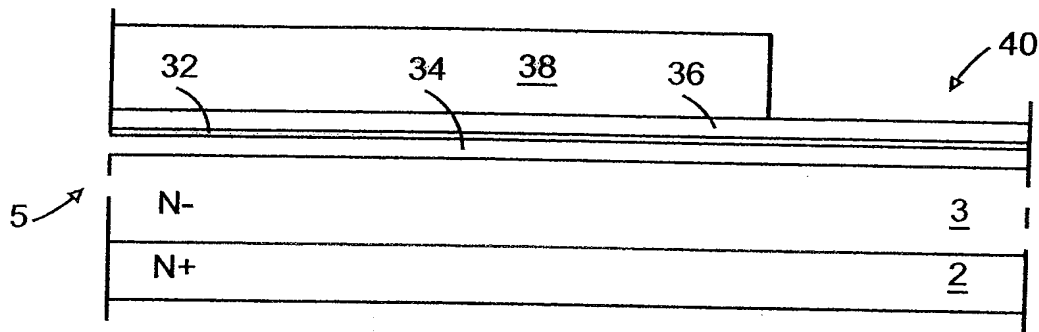


Figure 2b

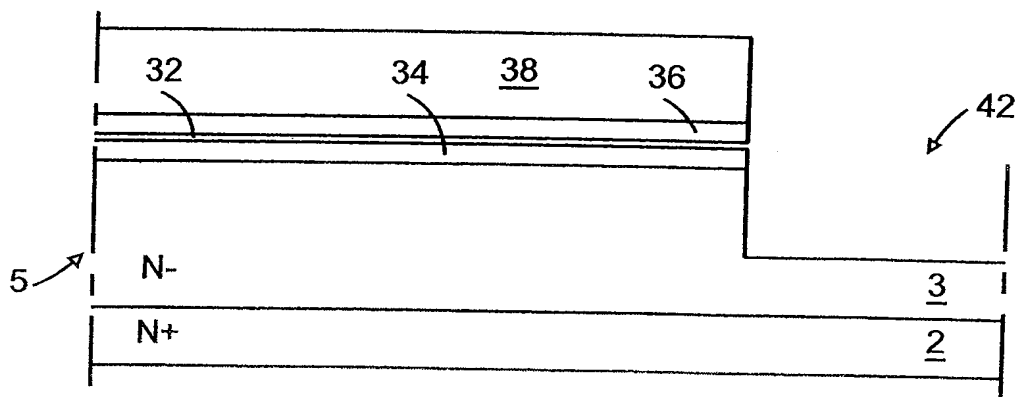


Figure 2c

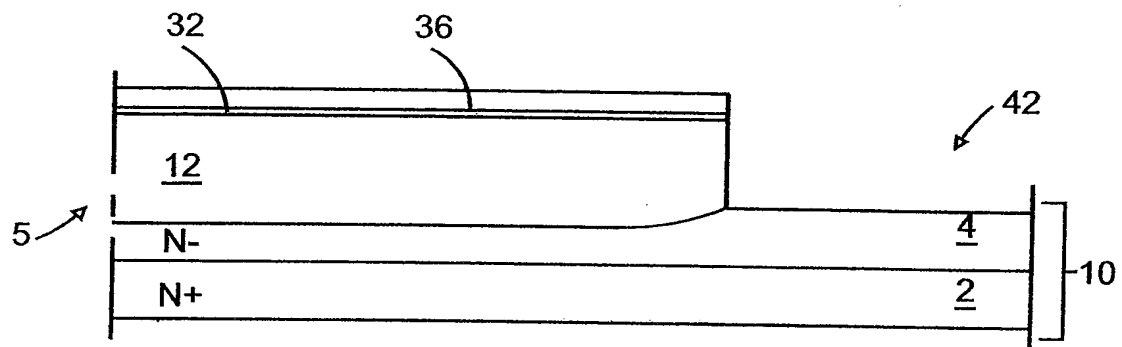


Figure 2d

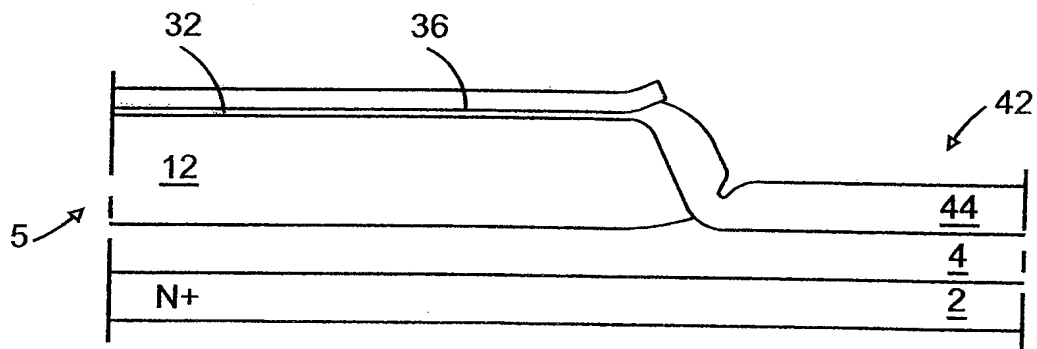


Figure 2e

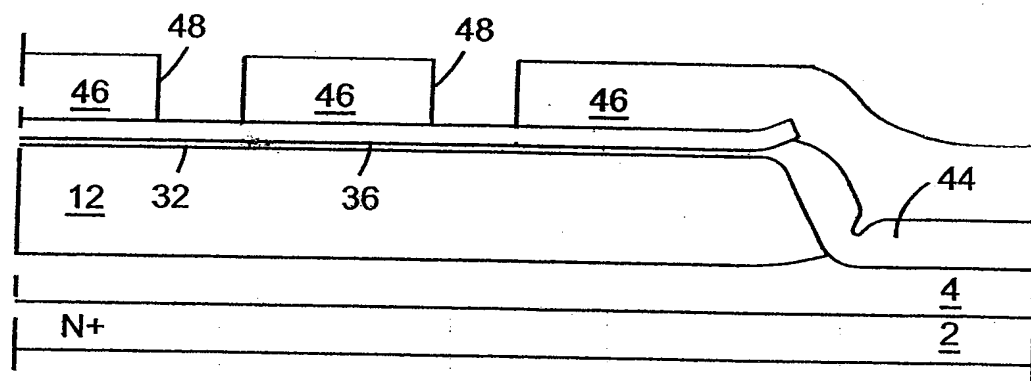


Figure 2f

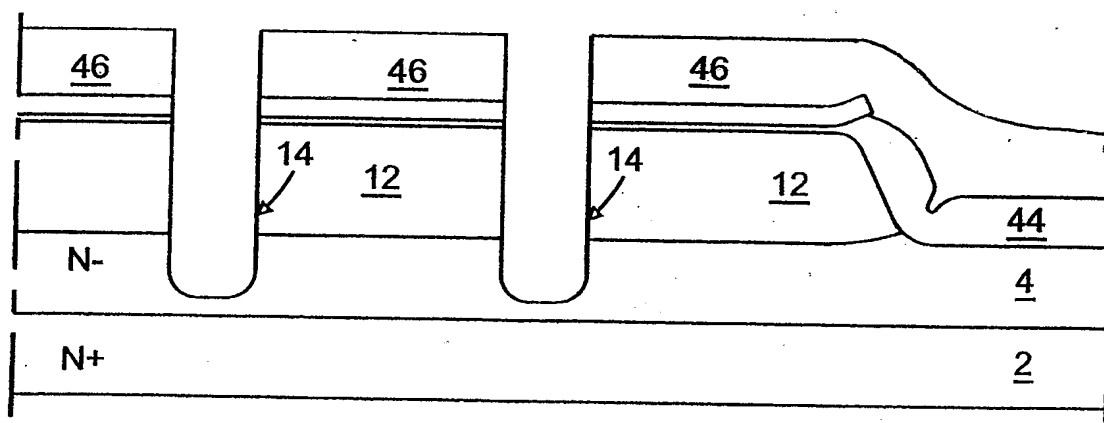


Figure 2g

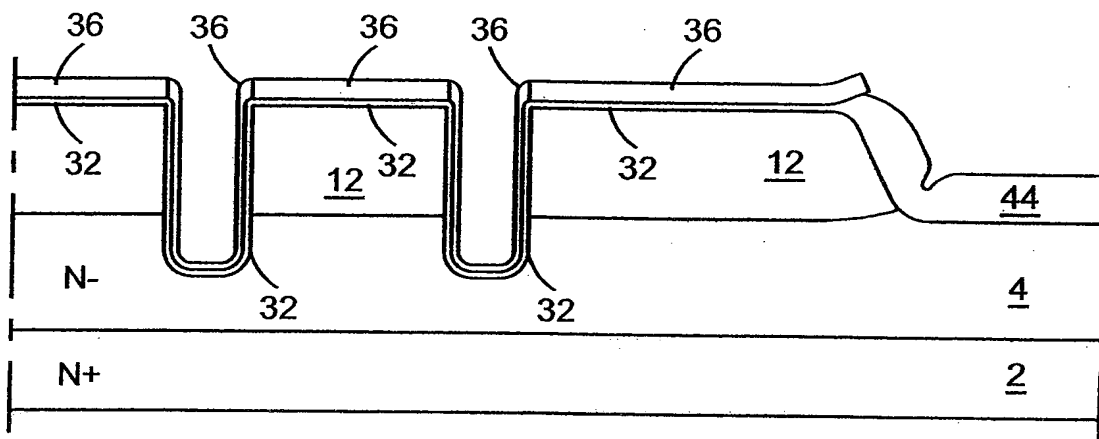


Figure 2h

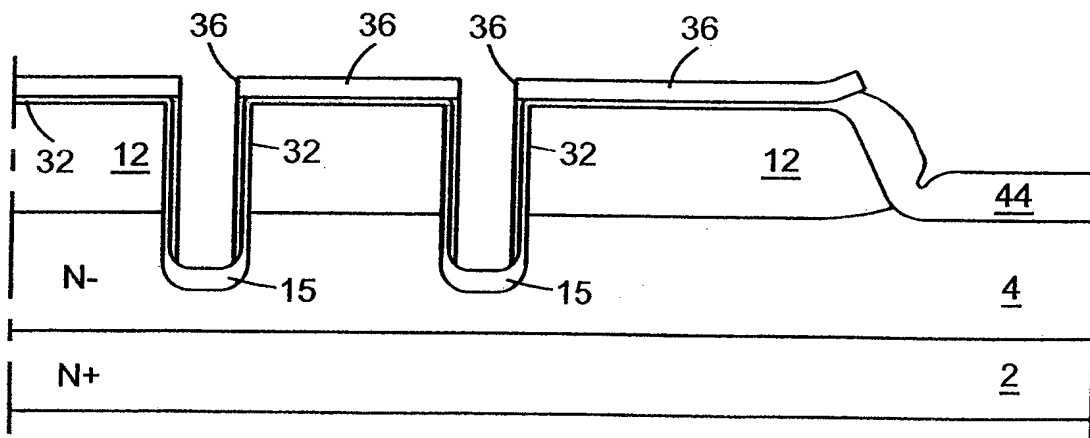


Figure 2i

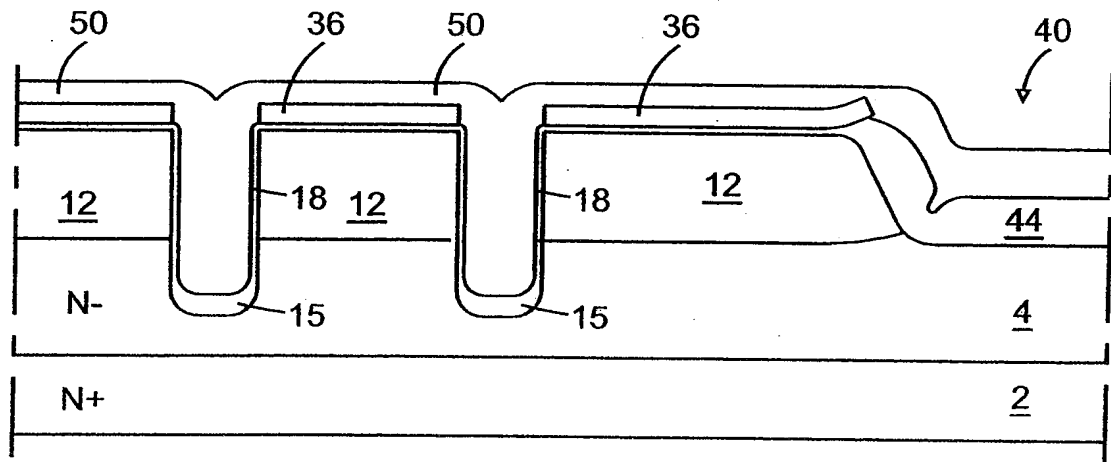


Figure 2j

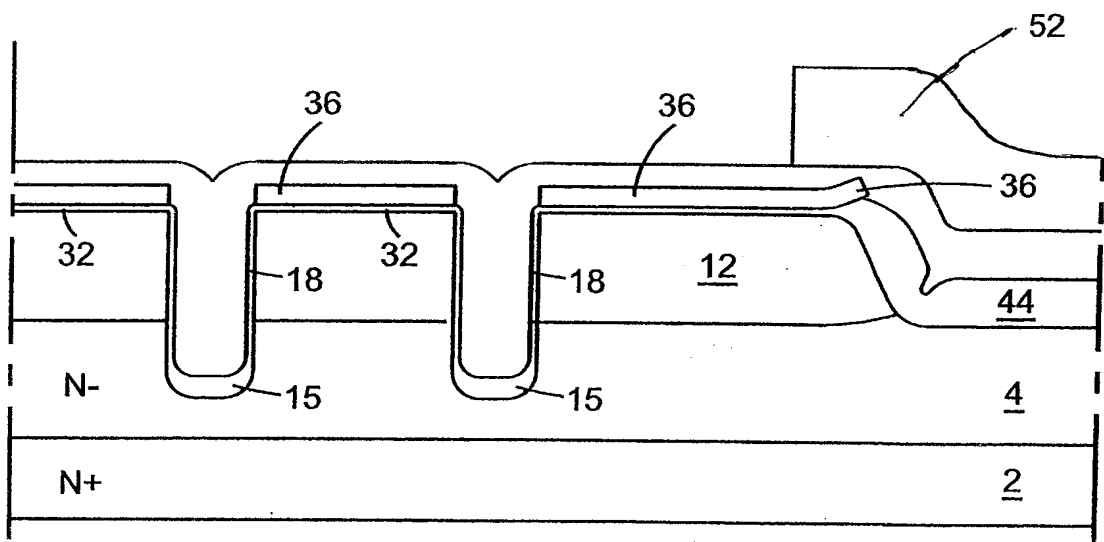


Figure 2k

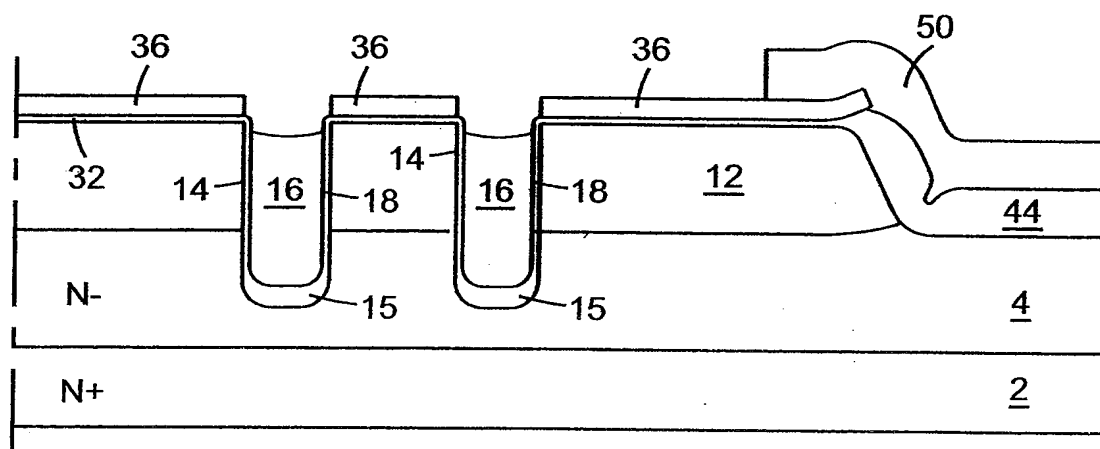


Figure 2l

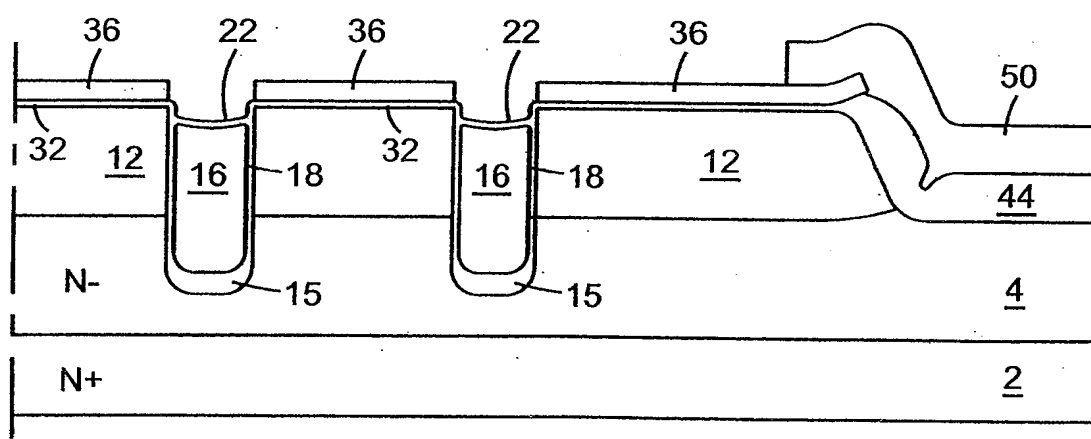


Figure 2m

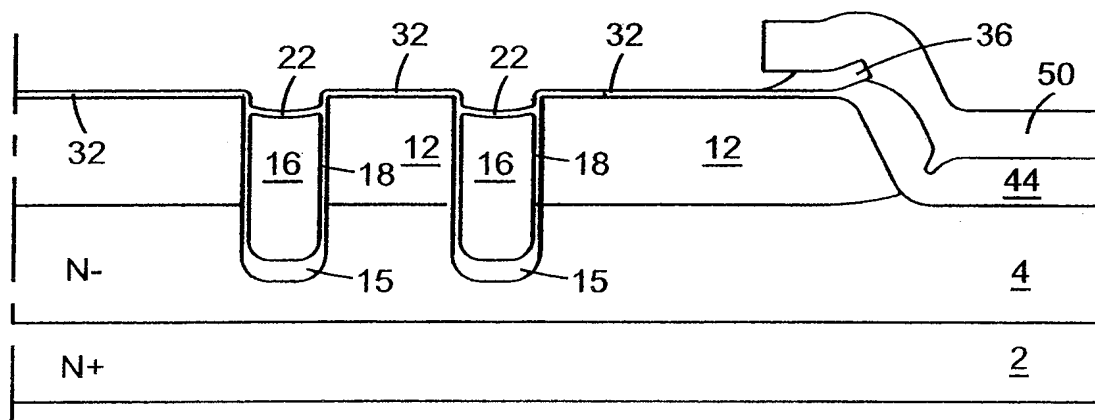


Figure 2n

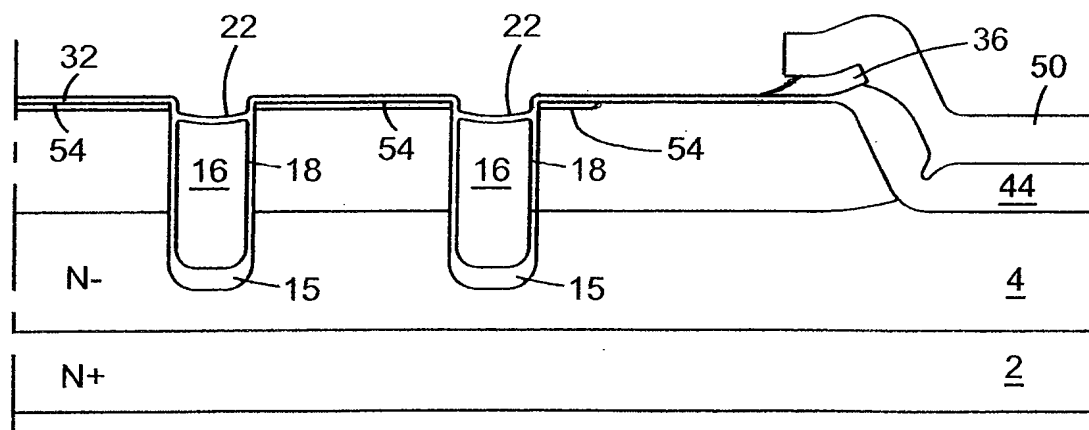


Figure 2o

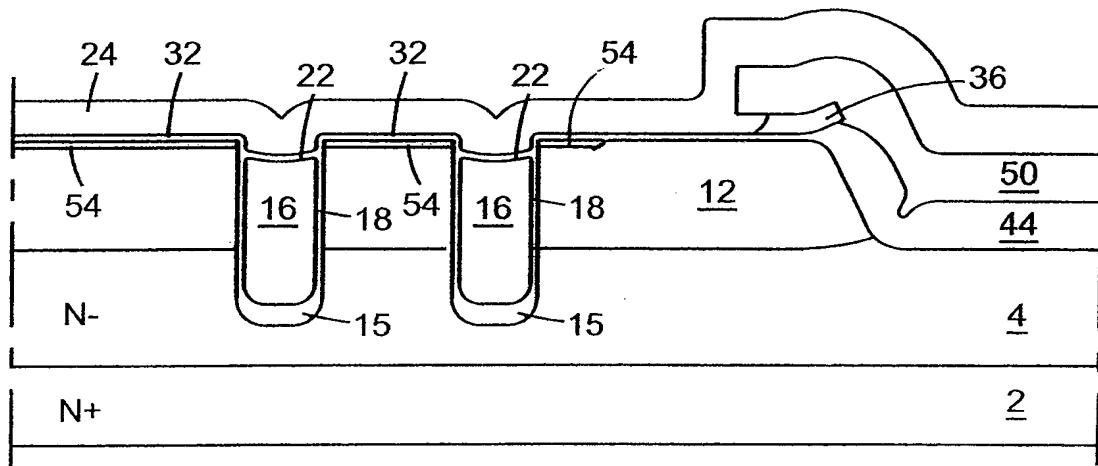


Figure 2p

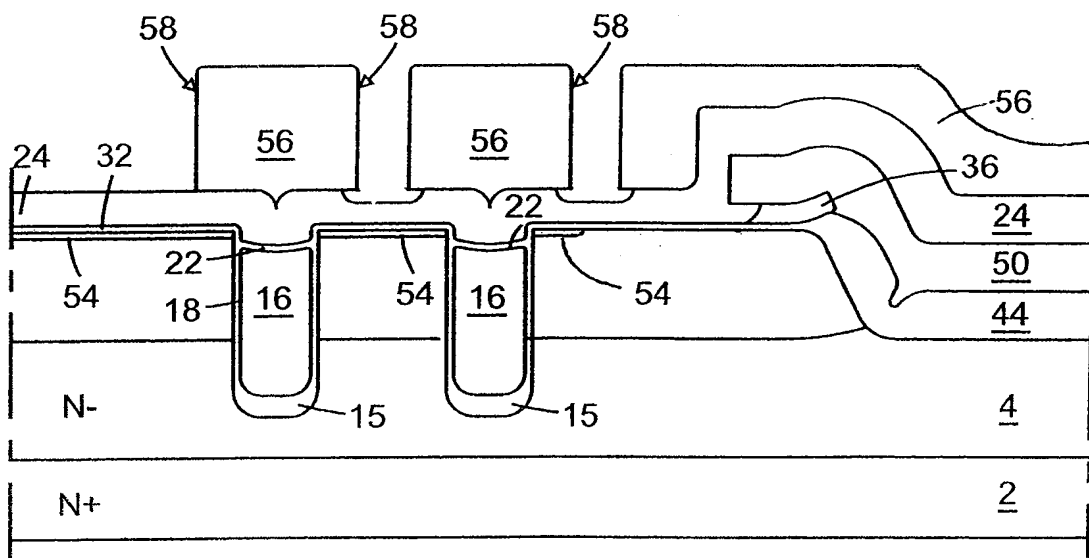


Figure 2q

[illegible]

Figure 2s

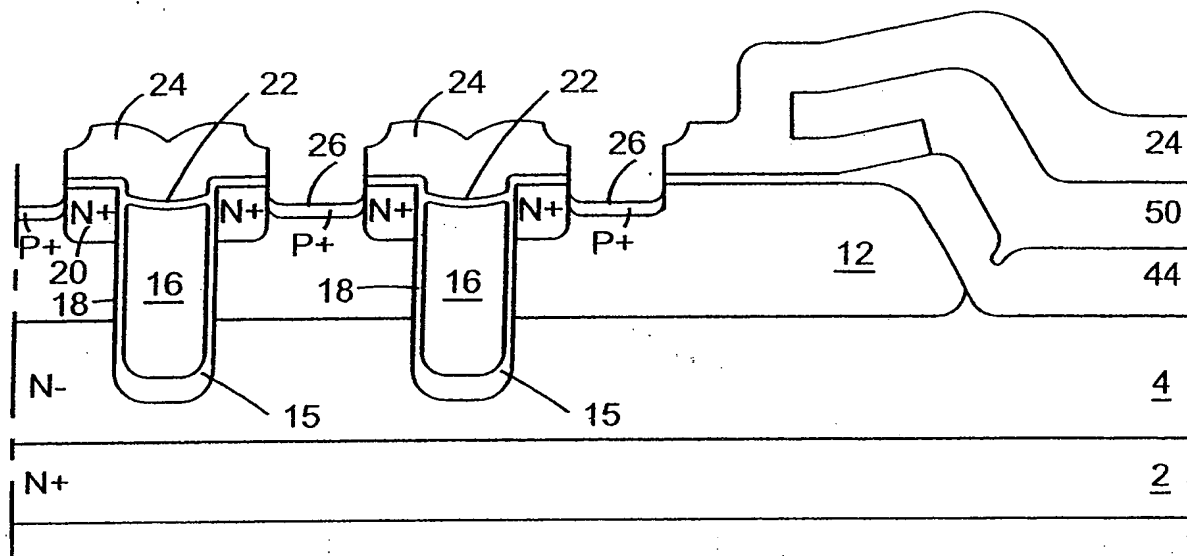


Figure 2t

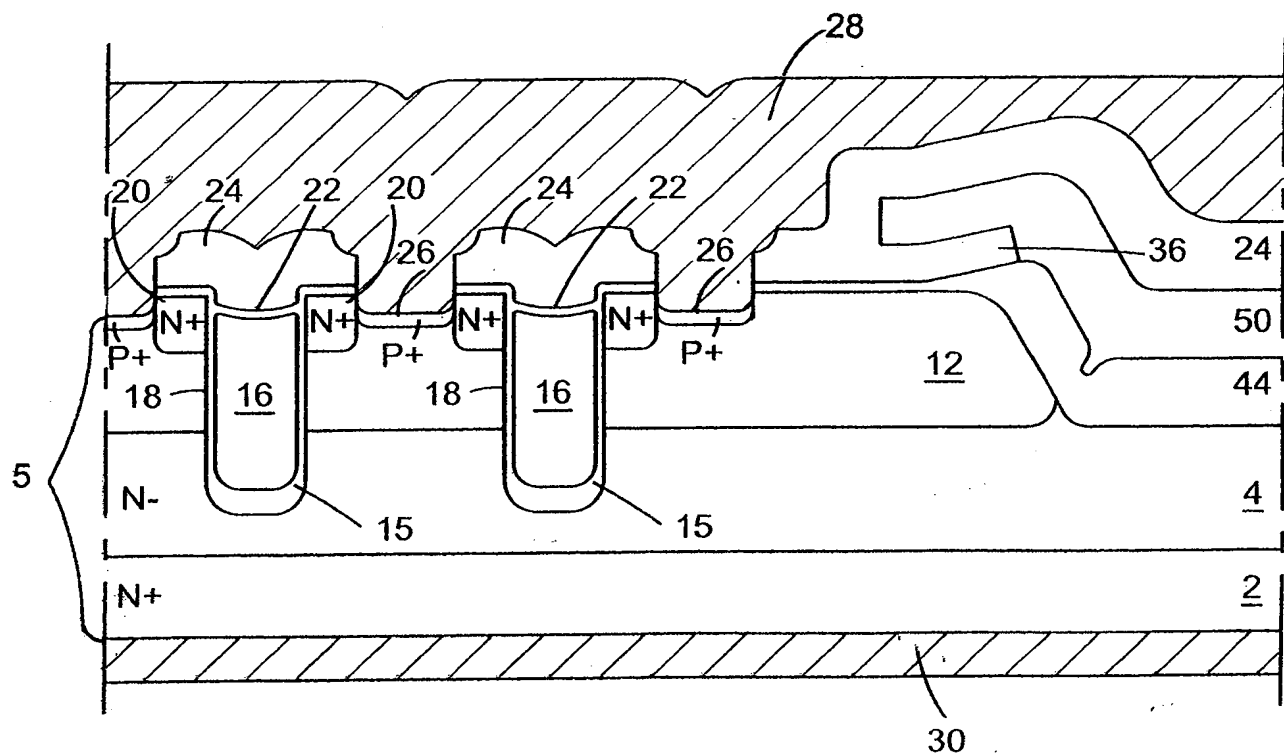


Figure 2u

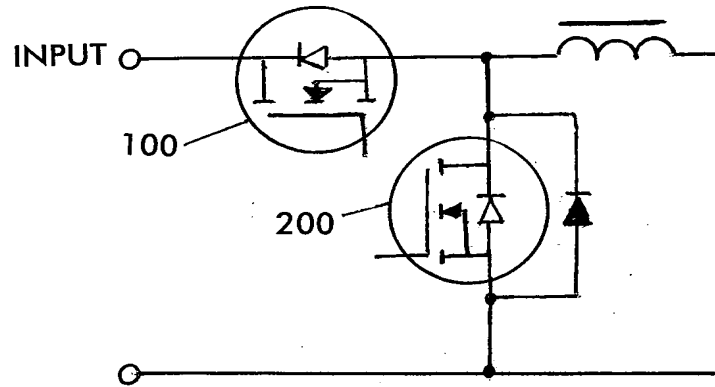


Figure 3
PRIOR ART

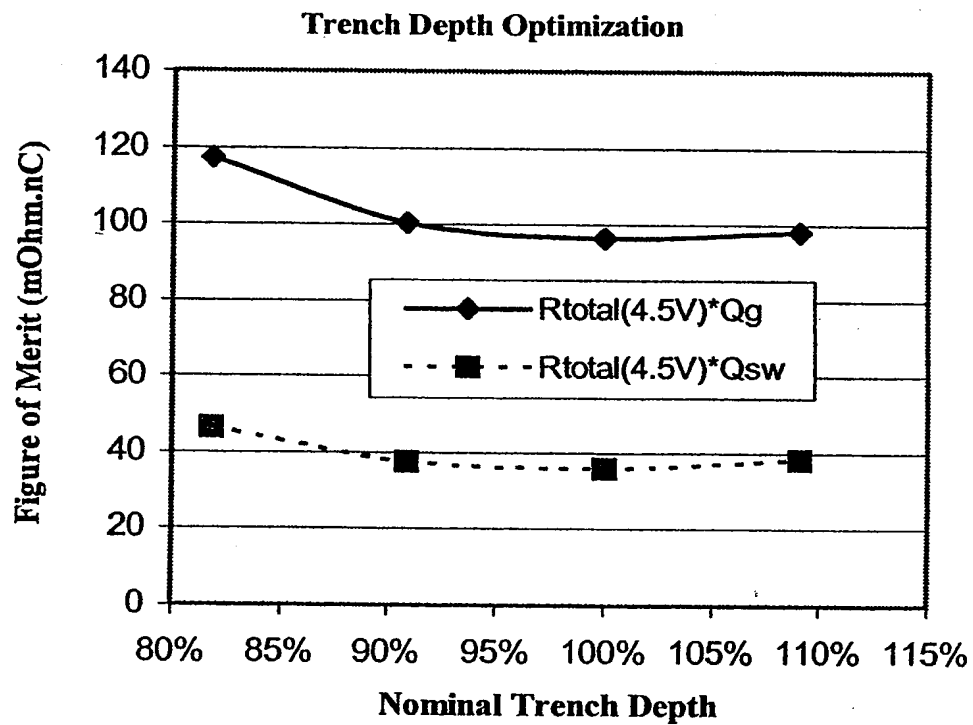


Figure 4 $R \cdot Q_g$ as a function of trench depth

Efficiency vs. Load
(12 V in, 1.7 V out, Freq 200kHz)

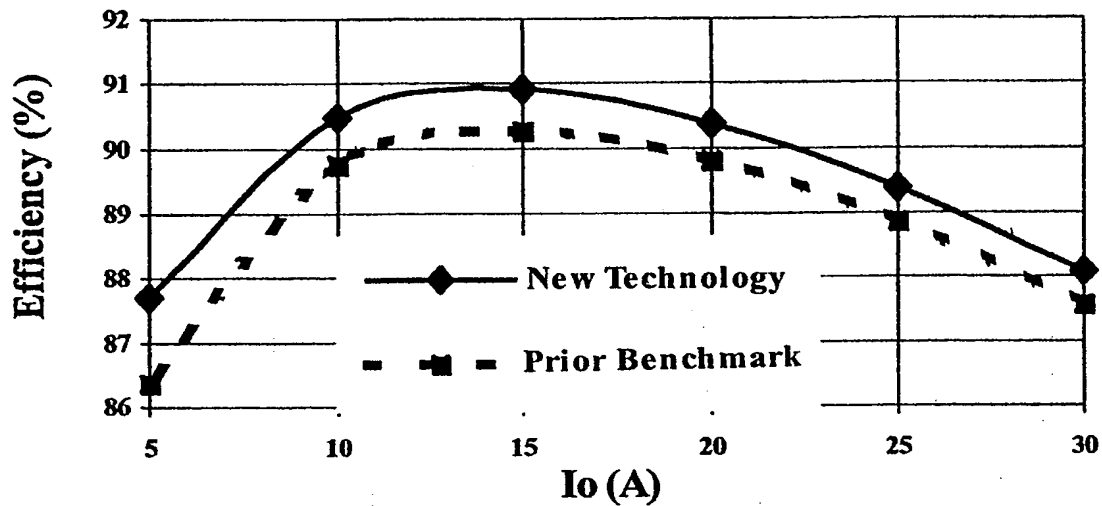


Figure 5a In-circuit efficiency comparison for the Control FET at 200kHz

Efficiency vs. Load
(12 V in, 1.7 V out, Freq 1Mhz)

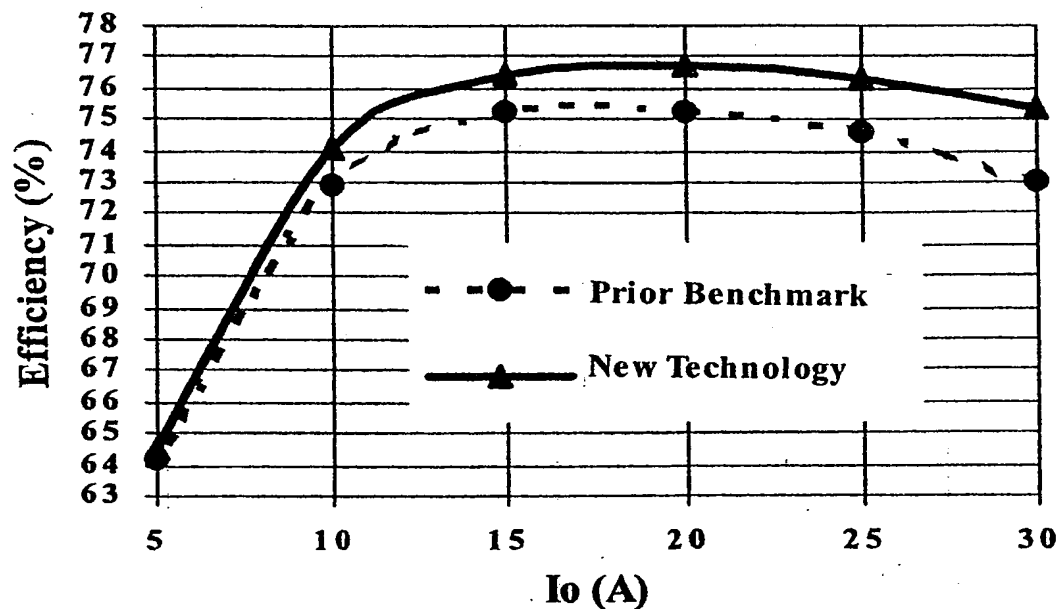


Figure 5b In-circuit efficiency comparison for the Control FET at 1MHz

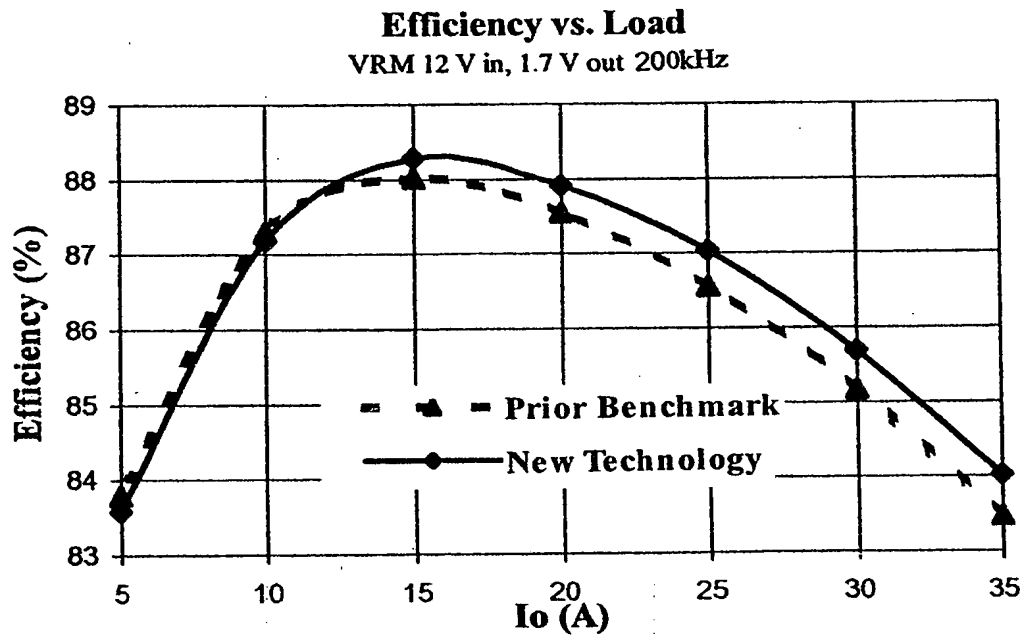


Figure 6a In-circuit efficiency comparison for the Sync FET at 200kHz

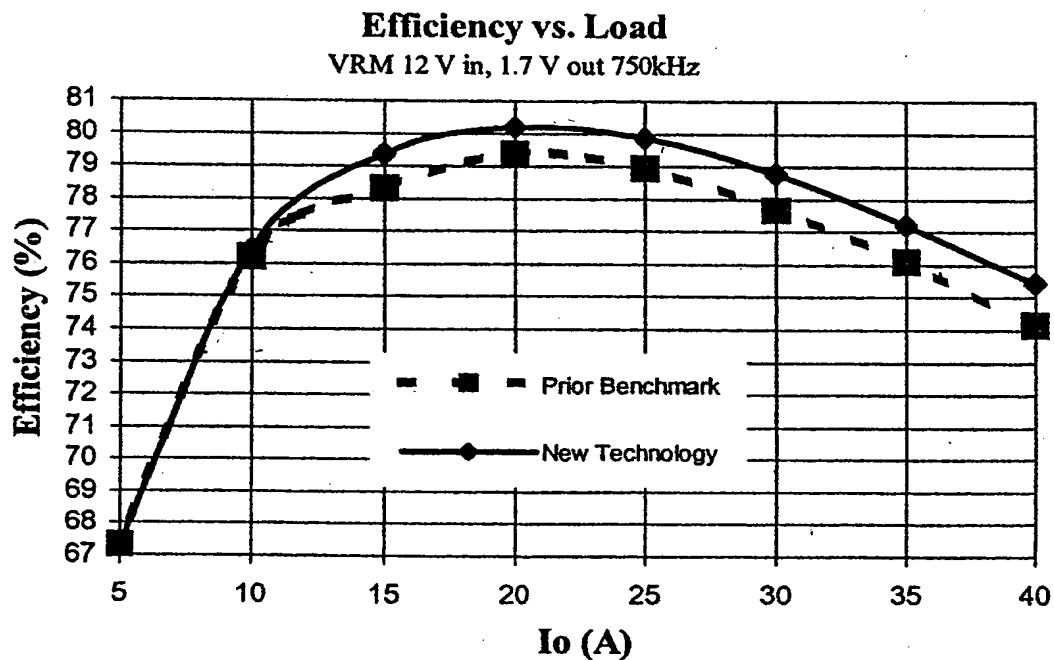


Figure 6b In-circuit efficiency comparison for the Sync FET at 750kHz